MINIA UNIVERSITY

Faculty of Engineering

CURRICULUM VITAE

Associate Prof. Dr. Hassan A. Youness
Chairman of CSE Dept.

PERSONAL DATA

FAMILY NAME	Youness
FIRST NAME	Hassan
NATIONALITY	Egyptian
SOCIAL STATUS	Married
RELIGION	Moslem
WORK ADDRESS	Minia University, Faculty of Engineering, Computers and Systems Dept.
E_mail	h_ali_h@yahoo.com, hassan_youness@mu.edu.eg
PROFESSION IN PASSPORT	Associate Professor
MILITARY SERVICES	Officer - Finish

EDUCATION

B.Sc. (Assiut University)

UNIVERSITY	Assiut
FACULTY	Engineering
DEGREE HELD	B.Sc.degree in Computer & Control Eng.
PROJECT SUBJECT	Local Area Network (LAN) using Assembly Language

M.Sc. (Assiut University)

POST GRADUATE	M.Sc. in Computer Engineering
GRADE OF PAVE MASTER	Excellent with 92.4%

PREPARATORY M.SC. COURSES AND GRADES :

Course	*Grade*
- Neural Networks	Very Good
- Fault-Tolerant in Computer Systems	Distinction
- Software Engineering	Distinction
- Special Purpose Electronics Digital Circuits	Distinction
- Computer Aided Design in Control Systems	Distinction

THESIS TITLE : **Algorithm-Based Fault Detection and Recovery on Multiprocessor Systems.**

Ph.D (Osaka University & Ain Shams University)

PREPARATORY Ph.D. COURSES :

- Image Processing and Computer Vision

- Advanced Computer Architecture
- Fuzzy Logic

QUALIFIED Ph.D COURSES :
- Computer Architecture
- Advancd Digital Design
- Advanced Control
- System Theory
- Distriputed Operating Systems
- Electronics
- System Level Design (Osaka University)

THESIS TITLE : **Suboptimal Hardware/Software Codesign in Digital Systems.**

LANGUAGES SKILLS Arabic – English (International TOEFL, IBT) - Japanese
OTHER SKILLS ICDL

PREVIOUS EMPLOYMENT

- Assistant Researcher in Electrical and Computer Systems in the Researh & Development (R & D) in Aluminium Company.
- Maintenance Engineer in Zonal Computer (IBM Private Company).
- Technical Support S/W and H/W for PC's.
- Instructor for computer Courses in IBM Private Company in Computer.
- Internet Instructor.
- Demonstrator at Faculty of Polly-Technic Education 1998-2000.
- Demonstrator at Electrical Engineering Dept., High Institute of Energy 2000-2001.
- Assistant Lecurer at Electrical Engineering Dept., High Institute of Energy 2001-2003.
- Ph.D researcher at Osaka University, Osaka, Japan 2007-2009.
- Researcher for a capacity building project, Mentor Graphics – Egypt, 2009-2011.

CURRENT EMPLOYMENT

- ***Chairman of Computers and Systems Engineering Dept***., Faculty of Engineering , Minia University from Sept., 2018 - Now.

COURSES ATTENDED

1- Database Informix programming (SQL & 4GL) Under UNIX system in ICL Company.
2- Teaching for computer Science in IBM Company.
3- Maintenance in EGYCOM Company.
4- Internetworking TCP/IP on MS Windows NT 4.0.
5- Supporting MS Windows NT Core Technology.
6- Windows NT Administration.
7- UNIX utilities & Commands.
8- UNIX System Administration.

TRAINING COURSES

1. Course No. 26 in pedagogy, 27, Sep., - 12, Oct., 2003.

2. Thinking Skills (P1), 11 – 14, Jul., 2005.
3. The development of literature and the ethics of the profession (P4), 15 – 17, Aug., 2005.
4. Development of effective communication skills (I2), 19 – 22, Sep., 2005.
5. Credit hour system (T2), 7 – 8, Jun., 2009.
6. Quality standards in the teaching process (T4), 16-17, June, 2009.
7. Effective Presentation (C2), 23 – 24, June, 2009.
8. Strategic Planning (L1), 2 - 3, Dec., 2013.
9. Organizing scientific conferences (C3), 9- 10, Dec., 2013.
10. Time Management and meetings (L4), 23 - 24, Dec., 2013.
11. The research team management (R2), 5 - 6, Jan., 2014.
12. Characterization of courses, 24-25, Feb., 2014.
13. Effective Education, 10 – 12, Mar., 2014
14. Evaluation continuous, 5-8, Apr., 2014
15. Ongoing review, 4-5, May, 2014.
16. The establishment of personal websites, 25-27, Oct., 2015.
17. Search databases and management of scientific references (Endnote), 1-3, Nov. 2015.

ACTIVITIES & PROJECTS

- Technical studying for offers for Computers and peripherals.
- Technical studying for offers for Internet and Networking.
- Techinal Project with Mentor Graphics, “Capacity Building Project for HW/SW Design”,2010.
- ICTP in Minia University, Faculty of Engineering.
- presenter at E-JUST 2016.

- B.Sc. Projects 2009, 2010.
  - Bank information Security system (BISS) , 2010
  - Optimized and portable Lab, 2010
  - Robot Aimer 2010

- B.Sc. Projects 2010, 2011.
  - Simultaneous localization and mapping (SLAM), 2011
  - CPU and GPU parallel programming, 2011
  - Interactive board (IB), 2011
  - Auto-mine detection Robot, 2011
  - Museum Security, 2011
  - Compact weather station controlled wireless, 2011

- B.Sc. Projects 2011, 2012.
  - Virtual Keyboard, 2012
  - Auto Pilot, 2012
  - Smart Voice Controlled WheelChair, 2012
  - Air Mouse and Multi Touch, 2012
  - Autonomous Visually Steered Vehicle, 2012

- B.Sc. Projects 2012, 2013.
  - Vending Machine, 2013

- Smart Elevator, 2013
- Rubik's Cube Solver, 2013

- B.Sc. Projects 2013, 2014.
  - Voting Machine system, 2014

- B.Sc. Projects 2014, 2015.
  - CUDA GPU system, 2015
  - Automated Fire Fiting System using GPS 2015

- B.Sc. Projects 2015, 2016.
  - V2V system, 2016
  - Intelligent Garbage basket with high performance material detection 2016

- B.Sc. Projects 2016, 2017.
  - IoT Intellegent Addressable Fire Fighting system Design, 2017

- B.Sc. Projects 2017, 2018.
  - Human Motion Tracking Using Low-Cost Wearable Sensor 6dof , 2018
  - Indoor SLAM Using Robot Operating System 2018

UNDERGRADUATE COURSES

- The Courses that I teach to Undergraduate Students : -
  - Digital design, (Undergraduate)
  - Microprocessor, (Undergraduate)
  - Computer Architecture, (Undergraduate)
  - Image Processing & Computer Vision, (Undergraduate)
  - Operating systems, (Undergraduate)
  - Software engineering, (Undergraduate)
  - Parallel computing, (Undergraduate)
  - Computer interface, (Undergraduate)
  - Lab. CSE 3rd Year (15 Experements) (Undergraduate)
  - Lab. CSE 4th Year (15 Experements) (Undergraduate)

POSTGRADUATE COURSES

- Embedded systems, (Postgraduate)
- Fault tolerant systems, (Postgraduate)
- Task Scheduling on Parallel Systems (Postgraduate)
- Network Computer Systems (Postgraduate)
- Advanced Computer Architecture (Postgraduate)
- nVidia GPGPU systems (Postgraduate)

PUBLISHED BOOKS

1. **Hassan YOUNESS** "HW/SW Codesign Techniques in Parallel Computing Systems", LAP LAMBERT Academic Publishing, ISBN-10: 3659131253, ISBN-13: 978-3659131257, May, 2012.

PUBLISHED PAPERS

Journals

1. **H. YOUNESS**, K. Sakanushi, Y. Takeuchi, A. Salem, A. Wahdan and M. Imai, "Optimal Scheme for Search State Space and Scheduling on Multiprocessor Systems", IEICE Transaction On Fundamentals of Electronics, Communications and Computer Sciences, Vol. E92-A, No.4, pp.1088-1015, Apr. 2009.

2. **H. YOUNESS**, Mohamed Osama and Aiman Tarek, "Load Balancing On Cpu-Gpu Heterogeneous System", JAUES, Al-Azhar University Engineering Journal, Vol. 7, No. 8, pp.199-205, Dec. 2012.

3. **H. YOUNESS**, Mahmoud Khaled and Mohamed Moness, "Quad-Core MPSoC Architecture for PID-Based Embedded Control Systems", International Journal of Computer Theory and Engineering IJCTE 2013 Vol.5(6):914-919 ISSN: 1793-8201, DOI: 10.7763/IJCTE.2013.V5.822, Dec. 2013.

4. **H. YOUNESS,** Mahmoud Khaled and Mohamed Moness, "Multiprocessor FPGAs and Microcontrollers for Embedded Control Systems: A Detailed Study", IEEE Transactions on Industrial Informatics, Volume:10, Issue: 4. DOI: 10.1109/TII.2014.2355036, Nov. 2014.

5. **H. YOUNESS,** "Architecture Level Model Design For Multi-Core Systems", Journal of Engineering Sciences, Assiut University, Faculty of Engineering, Vol. 42, No. 6, pp. 1378–13912014, Nov. 2014.

6. Hammam M.Abdelaala, Ahmed N.Elmahdya, Ali A.Halawaa, **Hassan A. YOUNESS**, "Improve the automatic classification accuracy for Arabic tweets using ensemble methods", Journal of Electrical Systems and Information Technology, ScienceDirect, Elsevier, April, 2018.

Conferences

7. **Hassan H. A.**, "An Efficient Algorithm-Based Fault Detection and Recovery on Multiprocessor Systems", Proceeding of the 6th IEEE International Conference on Electronics, Circuits and Systems (ICECS'99), Paphos, Cyprus, Sept., 1999, Vol. 2, pp. 1093-1096.

8. **Hassan H. A.**, "Analysis of Energy Consumption Pattern and Environmental Indicators for the Residental Sector of Aswan City" Organization for Energy Planning and, High Institute of Energy Conference, Egypt 2002.

9. **H. YOUNESS**, K. Sakanushi, Y. Takeuchi, A. Salem, A. Wahdan and M. Imai, "An Optimal Scheduling for Task Precedence Graphs on Parallel Systems Based on Geometric Analysis", IEEE Circuits and Systems, Karuizawa, pp. 605-610, Apr. 2008.

10. **H. YOUNESS**, K. Sakanushi, Y. Takeuchi, A. Salem, A. Wahdan, M. Moness and M. Imai, "A New Algorithm Based on Geometric Analysis for Task Scheduling in Multiprocessor Systems", First Egypt-Japan International Symposium on Science and Technology (EJISST2008), WASEDA

University, Tokyo, Japan, Jun., 2008.

11. **H. YOUNESS**, M. Hassan, K. Sakanushi, Y. Takeuchi, A. Salem, A. Wahdan, M. Moness and M. Imai, "A High Performance Algorithm for Scheduling and Hardware-Software Partitioning on MPSoCs", IEEE International Conference on Design and Technology of Integrated Systems in Nanoscale Era (DTIS), Cairo, Egypt, Apr., 2009.

12. **H. YOUNESS**, M. Hassan, K. Sakanushi, Y. Takeuchi, A. Salem, A. Wahdan, M. Moness and M. Imai, "Optimization Method for Scheduling Length and Number of Processors on Multiprocessor Systems", IEEE International Conference on Computer Engineering & Systems (ICCES), Cairo, Egypt, Dec. 2009.

13. **H. YOUNESS**, M. Hassan, K. Sakanushi, Y. Takeuchi, A. Salem, A. Wahdan, M. Moness and M. Imai, "Efficient Partitioning Technique on Multiple Cores Based on Optimal Scheduling and Mapping Algorithm", IEEE International Symposium on Circuits and Systems in Nano-Bio Circuit Fabrics and Systems (ISCAS), Paris, France, May, 2010.

14. **H. YOUNESS,** Mohammed HASSAN, Mona SAFAR and Ashraf SALEM, "Software Partitioning and Architecture Exploration Automation Using Vista Architect", White paper, Mentor Graphics, England, 2010.

15. **H. YOUNESS,** Mohammed HASSAN, Mona SAFAR and Ashraf SALEM, "A Design Space Exploration Methodology for Allocating Task Precedence Graphs to Multi-core System Architectures", IEEE International on The 22th International Conference on Microelectronics (ICM 2010), Cairo, Egypt, Dec., 2010.

16. Karim Yehia , Mona Safar, **Hassan Youness**, Mohamed AbdElSalam, and Ashraf Salem, "A design methodology for system level synthesis of multi-core system architectures", IEEE International Electronics, Communications and Photonics Conference (SIECPC'11), Saudi, Apr., 2011.

17. **H. YOUNESS,** Mohammed HASSAN, and Ashraf SALEM, "A novel approach for System level Synthesis of Multi-core System Architectures from TPG Models", The 9th ACS/IEEE International Conference On Computer Systems And Applications (AICCSA,11), Egypt, Saharm-Elsheikh June. , 2011.

18. **H. YOUNESS**, Mahmoud KHALED, and M. MONESS, "Direct Mapping of Digital PID Control Algorithm to a Custom FPGA-Based MPSoC, The Parallel Digital PID (PDPID) Controller", ICEENG-8, May 2012.
19. **H. YOUNESS**, M. Osama, and A. Tarek, "Load Balancing On Cpu-Gpu Heterogeneous System", Al-Azhar Engineering Twelfth International Conference, Dec. 2012.

20. **H. YOUNESS**, Mahmoud Khaled and Mohamed Moness, "Quad-Core MPSoC Architecture for PID-Based Embedded Control Systems", International Conference on Computers and Communication Devices (ICCCD) 2013, Jun. 2013.

21. **H. YOUNESS**, Aly Omar and Mohamed Moness, "Fault Tolerant Heterogeneous Scheduling for Precedence Constrained Task Graphs Using Simulated Annealing", IEEE International Conference on Computers and Engineering Systems (ICESS) 2013, page: 307-312, Nov. 2013.

22. **H. YOUNESS**, Aly Omar and Mohamed Moness, "Fault Tolerant Heterogeneous MPSOC Schedule Length Minimization Based on Platform Reliability", IEEE Second International Japan-Egypt Conference on Electronics, Communications and Computers (JEC-ECC), page: 113-118, Dec. 2013.

23. **H. YOUNESS**, M. Osama, A. Ibraheim and Mohamed Moness, "An Efficient Implementation of Ant Colony Optimization on GPU for the Satisfiability Problem", IEEE 23rd Euromicro International Conference on Parallel, Distributed and Network based Processing, Turku, Finland, (PDP 2015), pp. 230-235, Mar., 2015.

24. **H. YOUNESS**, A. Hussein, A. Mahfoz, "A new hardware/software partitioning technique", IEEE 10th International Conference on Computer Engineering & Systems (ICCES 2015), pp. 113-118, Dec. 2015.

25. K. Mostafa, A. Hussein, **H. YOUNESS**, M.Moness, "High Performance Reconfigurable Viterbi Decoder Design for Multi-Standard Receiver", 33rd IEEE National Radio Science Conference (NRSC 2016), Aswan, Egypt, pp. 249-256 , Feb., 2016.

26. **H. YOUNESS,** M. Moness, O. Shaban, A. Hussein, "Accelerated Processing Unit (APU) Potential N-body Simulation Case Study", IEEE 11th International Conference on Computer Engineering & Systems (ICCES 2016), Cairo, Egypt, pp. 110-115, Dec., 2016.

27. **H. YOUNESS**, M. Abas, "An aceleration Technique for Cone Beam CT Reproduction", IEEE 12th International Conference on Computer Engineering & Systems (ICCES 2017).

REVIEW PAPERS & DISTINGUISHED COMMITTEE MEMBER

1. **H. YOUNESS**, "4th IEEE International Conference on Innovations in Information Technology, (Innovations,07), Dubai, Nov. 18-20, 2007.

2. **H. YOUNESS**, "IEEE International Conference on Global Communications (GLOBECOM 2009), Honolulu, Hawaii, USA, 30 Nov.- 4 Dec., 2009.

3. **H. YOUNESS**, "IEEE International Symposium on Circuits and Systems, (ISCAS,11), Rio de Janeiro, Brazil, May. 15-18, 2011.

4. **H. YOUNESS**, "IEEE International Symposium on Circuits and Systems, (ISCAS,12), Seoul, Korea, May. 20-23, 2012.

5. **H. YOUNESS**, "3rd International Conference on Wireless and Optical Communications (ICWOC 2014) in Singapore, organized by IACSIT and International Journal of Computer and Communication Engineering (IJCCE). As **Distinguished Committee Members,** May 24-26, 2014.

6. **H. YOUNESS**, "6th International Conference on Communication Software and Networks(ICCSN 2014) in Singapore, organized by IACSIT. As **Distinguished Committee Members,** May 24-26, 2014.

7. **H. YOUNESS**, "3rd International Conference on Intelligent Information Processing (ICIIP 2014) in Singapore, organized by IACSIT and International Journal of Signal Processing Systems (IJSPS). **Distinguished Committee Members.** May 24-26, 2014.

8. **H. YOUNESS**, IEEE Transactions on Parallel and Distributed Systems (TPDS) 2014.

9. **H. YOUNESS**, IEEE Transaction on Industerial Informatics (TII) 2015.

10. **H. YOUNESS, "**4th International Conference on Intelligent Information Processing (ICIIP 2015) will be held in Chengdu, China organized by ASR, supported by IJSPS and JIII, and assited by UESTC**. Distinguished Committee Members.** June 6-7, 2015.

11. **H. YOUNESS,** 8th International Conference on Mechanical and Electrical Technology, (ICMET 2016), **Technical Committee**, July 2-6, 2016 | Sun Moon Lake, Taiwan, 2016.

12. **H. YOUNESS,** IEEE Trans on Computer (TC) 2016.

13. **H. YOUNESS,** IEEE Trans on Industerial Electronics (TIE) 2016.

14. **H. YOUNESS**, IEEE Transactions on Parallel and Distributed Systems (TPDS) 2016.

15. **H. YOUNESS**, IEEE Transactions on Systems, Man and Cybernetics Systems 2016.

16. **H. YOUNESS,** IEEE-ICEEE 2017 : 2017 4th International Conference on Electrical and Electronics Engineering (ICEEE 2017)-IEEE, **Technical Committee**, Apr 8, 2017 - Apr 10, 2017, Ankara, Turkey.

17. **H. YOUNESS**, 2nd International Conference on Design, Materials and Manufacturing (ICDMM 2017), **Technical Committee,** June 23-25, 2017, Beijing, China.

18. **H. YOUNESS**, 9th International Conference on Mechanical and Electrical Technology (ICMET 2017), June 23-25, 2017 | Beijing, China, **Technical Committee**.

19. **H. YOUNESS**, 4th Annual International Conference on Design, Manufacturing and Mechatronics [ICDMM2017], **Technical Committee,** May 26-28, Guangzhou, Guangdong, China.

20. **H. YOUNESS**, 2019 International Conference On Innovative Trends In Computer Engineering (ITCE'2019), 2–4 February, 2019 Aswan, Egypt. **Trackchair**, and **Technical Program Co-Chairs**

Speaker

1. Collaborative Conference on Robotics (CCR 2016), Phuket, Thailand.

2. 4th Annual World Congress of Smart Materials 2018, (WCSM2018), Venue: Osaka, Japan

ORAL PRESENTATION

1. **Hassan H. A.**, "Analysis of Energy Consumption Pattern and Environmental Indicators for the Residental Sector of Aswan City" Organization for Energy Planning and, High Institute of Energy Conference, Egypt 2002.

2. **H. YOUNESS**, K. Sakanushi, Y. Takeuchi, A. Salem, A. Wahdan and M. Imai, "An Optimal Scheduling for Task Precedence Graphs on Parallel Systems Based on Geometric Analysis", IEEE Circuits and Systems, Karuizawa, pp. 605-610, Apr. 2008.

3. **H. YOUNESS**, K. Sakanushi, Y. Takeuchi, A. Salem, A. Wahdan, M. Moness and M. Imai, "A New

Algorithm Based on Geometric Analysis for Task Scheduling in Multiprocessor Systems", First Egypt-Japan International Symposium on Science and Technology (EJISST2008), WASEDA University, Tokyo, Japan, Jun., 2008.

4. **H. YOUNESS**, M. Hassan, K. Sakanushi, Y. Takeuchi, A. Salem, A. Wahdan, M. Moness and M. Imai, "A High Performance Algorithm for Scheduling and Hardware-Software Partitioning on MPSoCs", IEEE International Conference on Design and Technology of Integrated Systems in Nanoscale Era (DTIS), Cairo, Egypt, Apr., 2009.

5. **H. YOUNESS**, M. Hassan, K. Sakanushi, Y. Takeuchi, A. Salem, A. Wahdan, M. Moness and M. Imai, "Optimization Method for Scheduling Length and Number of Processors on Multiprocessor Systems", IEEE International Conference on Computer Engineering & Systems (ICCES), Cairo, Egypt, Dec. 2009.

6. **H. YOUNESS,** Mohammed HASSAN, Mona SAFAR and Ashraf SALEM, "A Design Space Exploration Methodology for Allocating Task Precedence Graphs to Multi-core System Architectures", IEEE International on The 22th International Conference on Microelectronics (ICM 2010), Cairo, Egypt, Dec., 2010.

7. **H. YOUNESS,** Mohammed HASSAN, and Ashraf SALEM, "A novel approach for System level Synthesis of Multi-core System Architectures from TPG Models", The 9th ACS/IEEE International Conference On Computer Systems And Applications (AICCSA,11), Egypt, Dec., 2011.

8. **H. YOUNESS**, Mahmoud KHALED, and M. MONESS, "Direct Mapping of Digital PID Control Algorithm to a Custom FPGA-Based MPSoC, The Parallel Digital PID (PDPID) Controller", ICEENG-8, May 2012.

9. **H. YOUNESS**, M. Osama, and A. Tarek, "Load Balancing On Cpu-Gpu Heterogeneous System", Al-Azhar Engineering Twelfth International Conference, Dec. 2012.

10. **H. YOUNESS**, Mahmoud Khaled and Mohamed Moness, "Quad-Core MPSoC Architecture for PID-Based Embedded Control Systems", International Conference on Computers and Communication Devices (ICCCD) 2013, Jun. 2013.

11. **H. YOUNESS**, Aly Omar and Mohamed Moness, "Fault Tolerant Heterogeneous Scheduling for Precedence Constrained Task Graphs Using Simulated Annealing", IEEE International Conference on Computers and Engineering Systems (ICESS) 2013, page: 307-312, Nov. 2013.

12. **H. YOUNESS**, Aly Omar and Mohamed Moness, "Fault Tolerant Heterogeneous MPSOC Schedule Length Minimization Based on Platform Reliability", IEEE Second International Japan-Egypt Conference on Electronics, Communications and Computers (JEC-ECC), page: 113-118, Dec. 2013.

13. **H. YOUNESS**, M. Osama and Mohamed Moness, "An Efficient Implementation of Ant Colony Optimization on GPU for the Satisfiability Problem", IEEE 23rd Euromicro International Conference on Parallel, Distributed and Network based Processing, Turku, Finland, PDP, Mar., 2015.

14. **H. YOUNESS**, A. Hussein, A. Mahfoz, "A new hardware/software partitioning technique", IEEE 10th International Conference on Computer Engineering & Systems (ICCES 2015), pp. 113-118, Dec. 2015.

15. **H. YOUNESS,** M. Moness, O. Shaban, A. Hussein, "Accelerated Processing Unit (APU) Potential N-body Simulation Case Study", IEEE 11th International Conference on Computer Engineering & Systems (ICCES 2016).

16. **H. YOUNESS,** M. Abas, "An aceleration Technique for Cone Beam CT Reproduction", IEEE 12th International Conference on Computer Engineering & Systems (ICCES 2017).

CONFERENCES ATTENDED

1. High Institute of Energy Conference, Aswan, Egypt, May., **2002**.

2. IEEE Circuits and Systems, Karuizawa, Japan, Apr., **2008**.

3. First Egypt-Japan International Symposium on Science and Technology (EJISST2008), WASEDA University, Tokyo, Japan, Jun., **2008**.

4. IEEE International Conference on Design and Technology of Integrated Systems in Nanoscale Era (DTIS), Cairo, Egypt, Apr., **2009**.

5. IEEE International Conference on Computer Engineering & Systems (ICCES), Cairo, Egypt, Dec. **2009**.

6. IEEE International on The 22th International Conference on Microelectronics (ICM 2010), Cairo, Egypt, Dec., **2010**.

7. The 9th ACS/IEEE International Conference On Computer Systems And Applications (AICCSA,11), , Saharm-Elsheikh, Egypt, Jun., **2011**.

8. ICEENG-8, Alexandria, Egypt, May **2012**.

9. Al-Azhar Engineering 12th International Conference, Cairo, Egypt, Dec. **2012**.

10. International Conference on Computers and Communication Devices (ICCCD), Kuala Lambour, Malysia, Jun. **2013**.

11. IEEE International Conference on Computers and Engineering Systems (ICESS), Cairo, Egypt, Nov. **2013**.

12. IEEE Second International Japan-Egypt Conference on Electronics, Communications and Computers (JEC-ECC), Cairo, Egypt, Dec. **2013**.

13. IEEE International Conference on Computers and Engineering Systems (10th ICESS), Cairo, Egypt, Nov. **2015**.

14. IEEE International Conference on Computers and Engineering Systems (11th ICESS), Cairo, Egypt, Dec. **2016**.
15. IEEE International Conference on Computers and Engineering Systems (12th ICESS), Cairo, Egypt, Dec. **2017**.

AWARDED

1. Scholarship from Osaka University, Osaka, Japan to study PhD in Hardware/Software CoDesign Systems, Imai Lab., Apr.2007 – Apr.2009.
2. Award from The 2nd Invoation Day, Project, "Voting Machine system", 2014.
3. Award from IEEE MUSUB, Project, "Smart Elevator", 2013.
4. Award from Minia University for the Awarded of International Science Research Impact Factors in International Journals, 2014.

Masters Theses that certified under my Supervison:-

1. Mahmoud Khaled, "*Multiprocessor Implementations of Digital Controllers using Heterogeneous System*", the Best Master thesis for overall Minia University, 2014.
2. Ali Omar Abdelaziem, "*Fault Tolerant and Scheduling in Heterogeneous System*", 2014
3. Khloud Mousatfa, "*Viterbi Decoder Design for Software Defined Radio System*", 2015.
4. Mouhamed Osama, "*An Efficient Exploration Algorithm For Digital VLSI Systems and Its Implementation On Cpu-Gpu Heterogeneous Systems*", 2015.
5. Amal Mahfuz, "*An Efficient HW/SW Partitioning Technique and its Application on Embedded Systems*", 2017.
6. Omar Shabaan, "*APU/GPU Heterogeneous Systems Trade offs*", 2018.

Yours faithfully

Hassan Ali Hassan Ahmed Youness